

New PHY, Link Layer Test Specifications from VESA® Designed to Ensure Interoperability, Compliance, with DisplayPort™ Standard

Milpitas, Calif., Feb. 17, 2009 – The Video Electronic Standards Association (VESA) has released comprehensive new test specifications covering the DisplayPort Physical Layer (PHY) and DisplayPort Link Layer, which provide test procedures designed to ensure compliance of DisplayPort devices and interoperability between them.

The VESA DisplayPort PHY Compliance Test Specification Version 1.1 details the required tests, test methods, conditions, and equipment requirements for interoperability. The specification establishes a superset test regimen for the determination of compliance of DisplayPort devices, segmented into source, receiver, copper cable, hybrid devices, and tethered devices. It stipulates the high level requirements for each test for device setup, test equipment, signal conditions, methodology and device to test instrument connectivity. It also incorporates test limits for the measurement results.

The VESA DisplayPort Link Layer Test Specification Version 1.1 defines a compliance test procedure and criteria (or masks) to maximize interoperability of DisplayPort devices at the Link Layer and above. It specifies the Link Layer (and above) tests for source, sink and branch devices.

“These new PHY and Link Layer Test Specifications will enhance and expand our compliance and interoperability efforts, which are vital to the long term success of DisplayPort,” said Bill Lempesis, VESA executive director. “Products that meet the requirements are certified, assuring users and customers that DisplayPort products meet the highest levels of compliance and interoperability.”

The PHY and Link Compliance Test Specifications are available at no charge and can be downloaded from <http://www.displayport.org> or <http://www.vesa.org>